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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/TD/	1	ROBERT F. PIERRET, "Semiconductor Device Fundamentals," Prentice Hall, 2nd Revised Edition, 1995	
/TD/	2	ANDREW MARSHAL and SREEDHAR NATARAJAN, " SOI Design: Analog, Memory and Digital Techniques," Springer, 2001	
/TD/	3	Sleight, J. W., et al. "A High Performance 0.13 μ m SOI CMOS Technology with a 70 nm Silicon Film and with a Second Generation Low-k Cu BEOL," Electron Device Meeting, 2001	
/TD/		IEDM Technical Digest, page 11.3.1-11.3 (2001)	

Examiner Signature	/Thomas Dickey/	Date Considered	07/03/2008
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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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